2187

AMENDMENT TRANSMITTAL LETTER			Docket No. M4065.0290/P290
Application No. 09/641,519	Filing Date August 21, 2000	Examiner B. Peugh	Art Unit 2187

Applicant(s): Kevin J. Ryan

Invention: DRAM WITH HIDDEN REFRESH

TO THE COMMISSIONER FOR PATENTS

Transmitted herewith is an Amendment in the above-identified application.

The fee has been calculated as shown below.

CLAIMS AS AMENDED					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	56	- 56 =	0	x 18.00	0
Independent Claims	9	- 9 =	0	x 84.00	0
Multiple Dependent Claims (check if applicable)			☐	280.00	0
Other fee (please specify):					0
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:					0

[x] Large Entity   ☐ Small Entity

[x] No additional fee is required for this Amendment.

☐ Please charge Deposit Account No. _____ in the amount of $ _____ .
A duplicate copy of this sheet is enclosed.

☐ A check in the amount of $ _____ to cover the filing fee is enclosed.

☐ Payment by credit card. Form PTO-2038 is attached.

[x] The Commissioner is hereby authorized to charge and credit Deposit Account No. __04-1073__
as described below.

   [x] Credit any overpayment.

   [x] Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.

Dated: _____April 28, 2003_____

_____
Thomas J. D'Amico
Attorney Reg. No.: 28,371

DICKSTEIN SHAPIRO MORIN & OSHINSKY LLP
2101 L Street NW
Washington, DC 20037-1526
(202) 828-2232

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Technology Center 2100

Docket No.: M4065.0290/P290
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Kevin J. Ryan

Application No.: 09/641,519   Group Art Unit: 2187

Filed: August 21, 2000   Examiner: B. Peugh

For: DRAM WITH HIDDEN REFRESH

<u>AMENDMENT</u>

Commissioner for Patents
Washington, DC 20231

Dear Sir:

In response to the Office Action dated January 27, 2003 (Paper No. 6), please amend the above-captioned application as follows:

IN THE CLAIMS:

Replace claims 15, 37 and 47 with amended claims 15, 37 and 47 below.

15. (Amended) The system of claim 13 further comprising a memory source other than said memory cells such that said controller operates said DRAM such that a conflicting data access command is satisfied with said memory source other than said memory cells and wherein said memory cells may be refreshed.

37. (Amended) The integrated circuit device of claim 35, wherein said system further comprises a memory source other than said memory cells such that upon said controller determining that data stored in at least some of said memory cells is in danger of